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( **Y2K Compliance** ) "

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( **BSI DISC PD2000** **BS 0 : 1997** )

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**Set Roll**

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( CHRONOLOGY )

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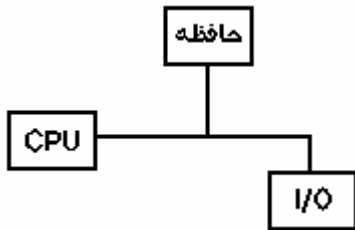
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Interrupt Controller  
 Timer  
 RTC

CPU

	ROM	ROM - BIOS
Setup	E2PROM	CMOS - ROM
	RAM	RAM



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Vendor

Timer  
 RTC  
 BIOS

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**END USER LEVEL**

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**Application**

**BIOS**

**RTC**

**BIOS**

**RTC**

**Chip**

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